

PLAN OF STUDIES

FULL-TIME STUDIES; FIRST-CYCLE STUDIES

ACADEMIC YEAR: 2025/2026
 SEMESTER: WINTER
 STUDY CYCLE: I (BSc)
 FIELD OF STUDY:

ELECTRONICS AND TELECOMMUNICATIONS YEAR II, SEMESTER 3

		17					
Day	Unit	cycle/year/semester		II/3		10	
		Hour	Group	ET-1			
MONDAY	1	08:00	LAB 1				
		Odd weeks	DIGITAL SYSTEMS DESIGN (C)				
		Even weeks	mgr inż. Salim Janji				
	2	09:30	P-201				
		09:45	Digital systems design (L)				
		Odd weeks	dr hab. inż. Maciej Krasicki				
	3	Even weeks	P-114				
		11:15					
		11:45	FOUNDATIONS OF TELECOMMUNICATION (L)				
	4	Odd weeks	dr inż. Michał Kasznia				
		Even weeks	P-243				
		13:15					
	5	13:30					
		Odd weeks					
Even weeks		dr inż. Michał Kasznia,					
6	15:00	Foundations of telecommunication (C)					
	15:10						
	Odd weeks						
7	Even weeks						
	16:40						
	16:50						
8	Odd weeks						
	Even weeks						
	18:20						
9	18:30						
	Odd weeks						
	Even weeks						
10	20:00						
	08:00	Programming in C++ (LAB)					
	Odd weeks	mgr Damian Stolarski					
11	Even weeks	P-020a					
	09:30						
	09:45	ELECTROMAGNETIC WAVES (L)					
12	Odd weeks	dr inż. Jarosław Szóstka					
	Even weeks	P-011					
	11:15						
13	11:45	Electromagnetic waves (LAB)					
	Odd weeks	dr inż. Jarosław Szóstka					
	Even weeks	P-002					
14	13:15						
	13:30	PROBABILISTIC METHODS IN E&T (L)					
	Odd weeks	dr Joanna Weissenberg					
15	Even weeks	A3-116					
	15:00						
	15:10	dr Joanna Weissenberg					
16	Odd weeks	Probabilistic methods in E&T (C)					
	Even weeks	A3-116					
	16:40						
17	16:50						
	Odd weeks						
	Even weeks						
18	18:20						
	18:30						
	Odd weeks						
19	Even weeks						
	20:00						

WEDNESDAY	1	08:00 Odd weeks Even weeks 09:30	
	2	09:45 Odd weeks Even weeks 11:15	
	3	11:45 Odd weeks Even weeks 13:15	
	4	13:30 Odd weeks Even weeks 15:00	PROGRAMMING IN C++ (L), dr inż. Maciej Sobieraj A3-108
	5	15:10 Odd weeks Even weeks 16:40	SYSTEM THEORY (L) dr hab. inż. Damian Karwowski A3-108
	6	16:50 Odd weeks Even weeks 18:20	
	7	18:30 Odd weeks Even weeks 20:00	
THURSDAY	1	08:00 Odd weeks Even weeks 09:30	
	2	09:45 Odd weeks Even weeks 11:15	
	3	11:45 Odd weeks Even weeks 13:15	
	4	13:30 Odd weeks Even weeks 15:00	
	5	15:10 Odd weeks Even weeks 16:40	
	6	16:50 Odd weeks Even weeks 18:20	
	7	18:30 Odd weeks Even weeks 20:00	
FRIDAY	1	08:00 Odd weeks Even weeks 09:30	System theory (LAB) mgr inż. Zofia Planner-Graca P-028
	2	09:45 Odd weeks Even weeks 11:15	SEMICONDUCTOR DEVICES AND ANALOG ELECTRONICS (L) dr inż. Krzysztof Klimaszewski P-114
	3	11:45 Odd weeks Even weeks 13:15	Semiconductor devices and analog electronics (LAB) mgr inż. Mateusz Lorkiewicz P-013
	4	13:30 Odd weeks Even weeks 15:00	45 hours/semester
	5	15:10 Odd weeks Even weeks 16:40	
	6	16:50 Odd weeks Even weeks 18:20	

EGZAMIN